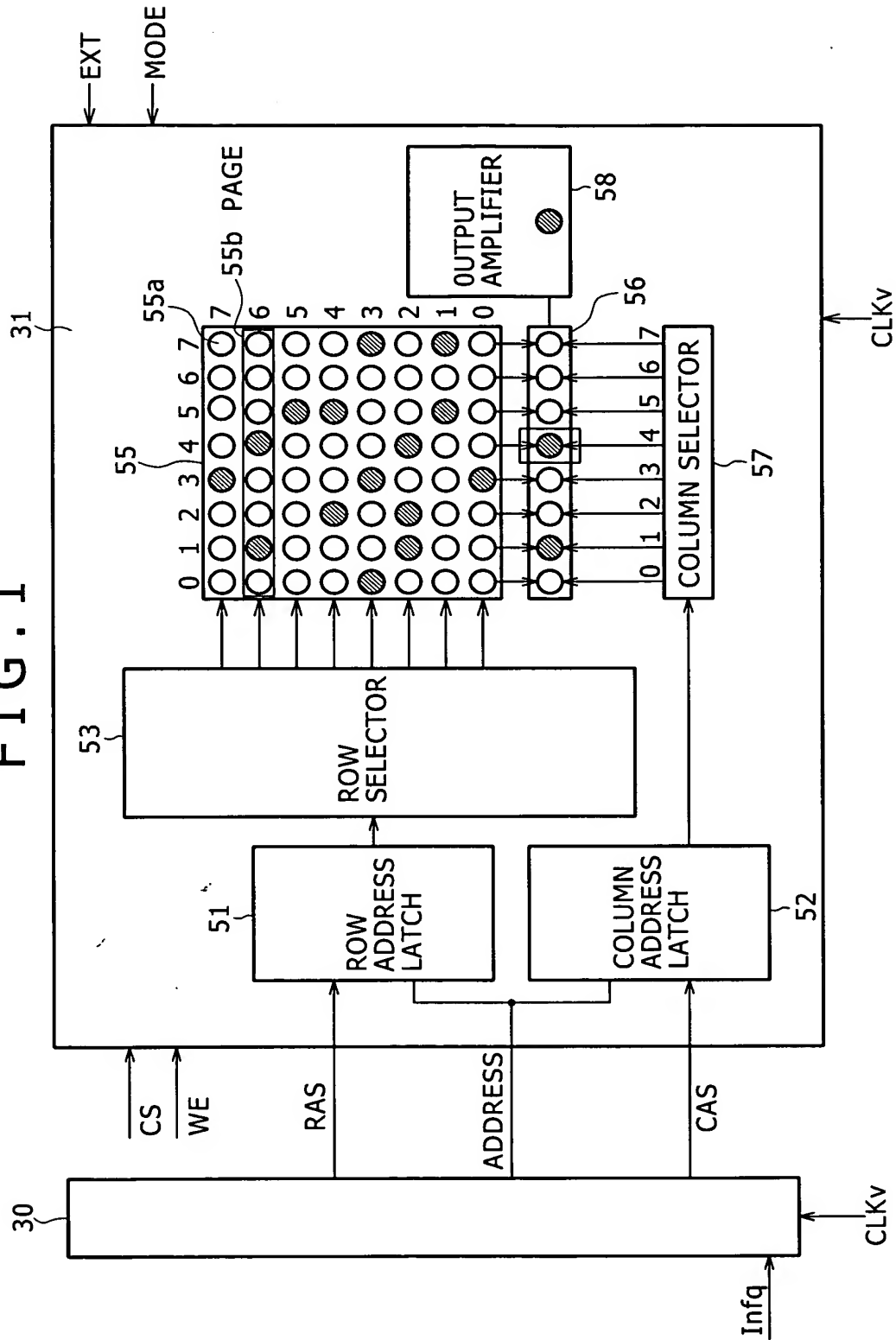


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FIG. 1



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FIG. 2

TYPICAL RELATIONSHIP BETWEEN SIGNALS AND COMMANDS										
COMMAND	OPERATION	CS	RAS	CAS	WE	EXT	MODE	ROW ADDRESS	BANK	COLUMN ADDRESS
DESL	NO-OPERATION	H	X	X	X	X	X	X	X	X
NOP	NO-INSTRUCTION	L	H	H	H	H	X	X	X	X
READ	READ	L	H	L	H	H	X	X	V	V
WRITE	WRITE	L	H	L	L	H	X	X	V	V
ACT	ACTIVATE	L	L	H	H	H	X	V	V	X
PRE	PRECHARGE	L	L	H	L	H	X	V	V	X
PALL	ALL-BANK PRECHARGE	L	L	H	L	H	X	X	X	X
REF	REFRESH	L	L	L	H	H	X	X	X	X
MRS	MODE SET	L	L	L	L	H	X	*	*	*
BST	BURST STOP	L	H	H	L	H	X	X	X	X
EXTENDED COMPLEX OPERATIONS	P+A+R	L	X	X	H	L	L	V	V+	V
	P+A+W	L	X	X	L	L	L	V	V+	V
	A+R	L	X	X	H	L	H	V	V+	V
	A+W	L	X	X	L	L	H	V	V+	V

SYMBOL	MEANING
L	0
H	1
X	ARBITRARY
V	VALID (V+MAY REQUIRE TWO TIMES AMOUNT OF INFORMATION.)
*	PARTIALLY VALID

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FIG. 3A

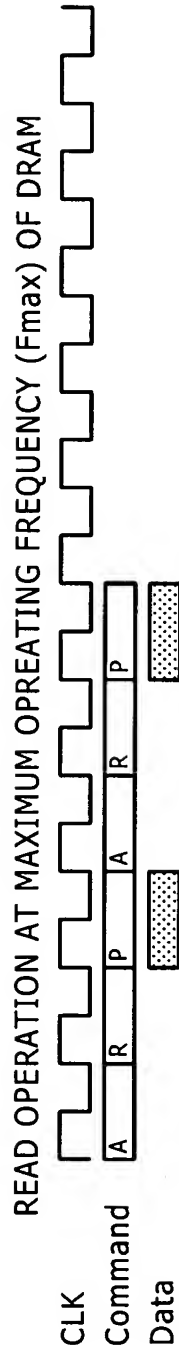


FIG. 3B

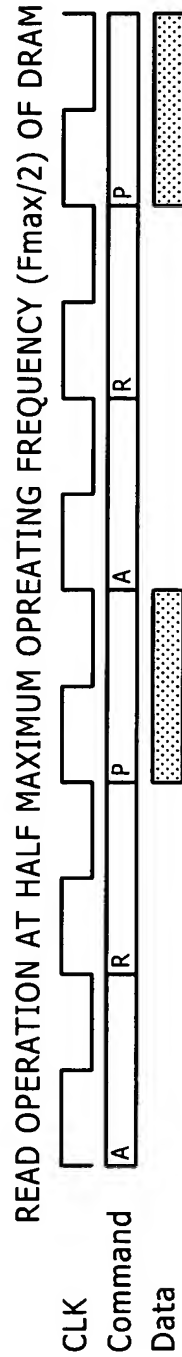
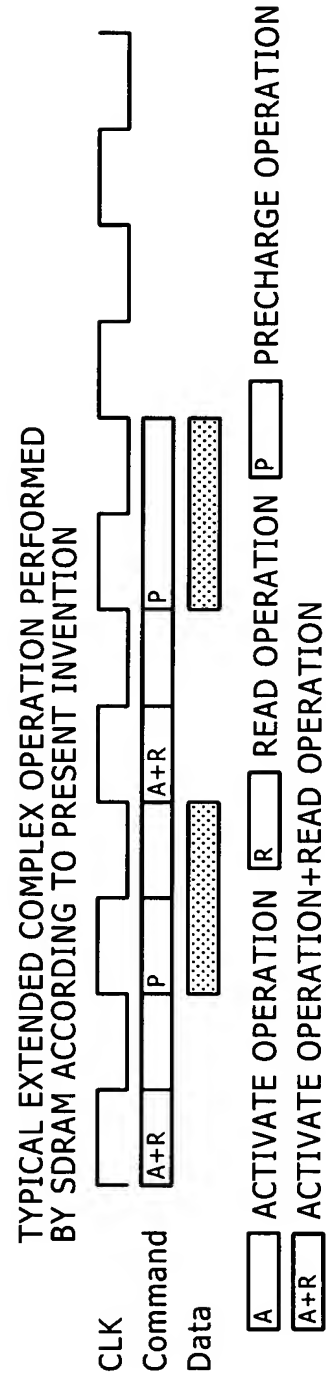


FIG. 3C



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FIG. 4A

DRAM READ OPERATION FOR PERFORMING ACTIVATE+READ+PRECHARGE
OPERATION WITHIN ONE CLOCK

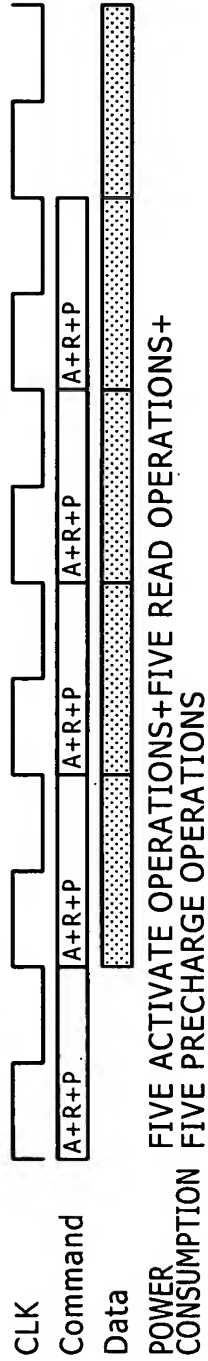
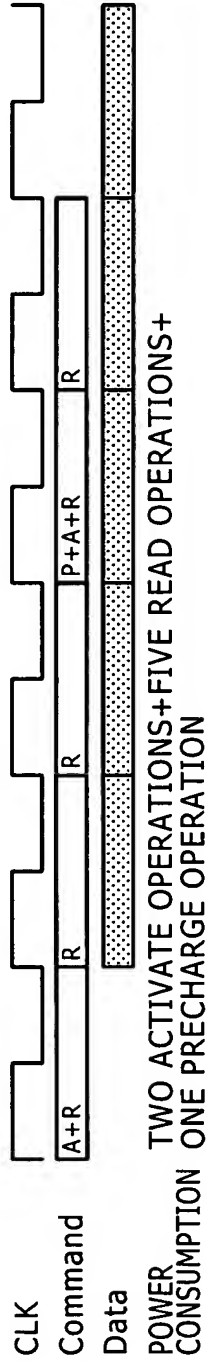


FIG. 4B

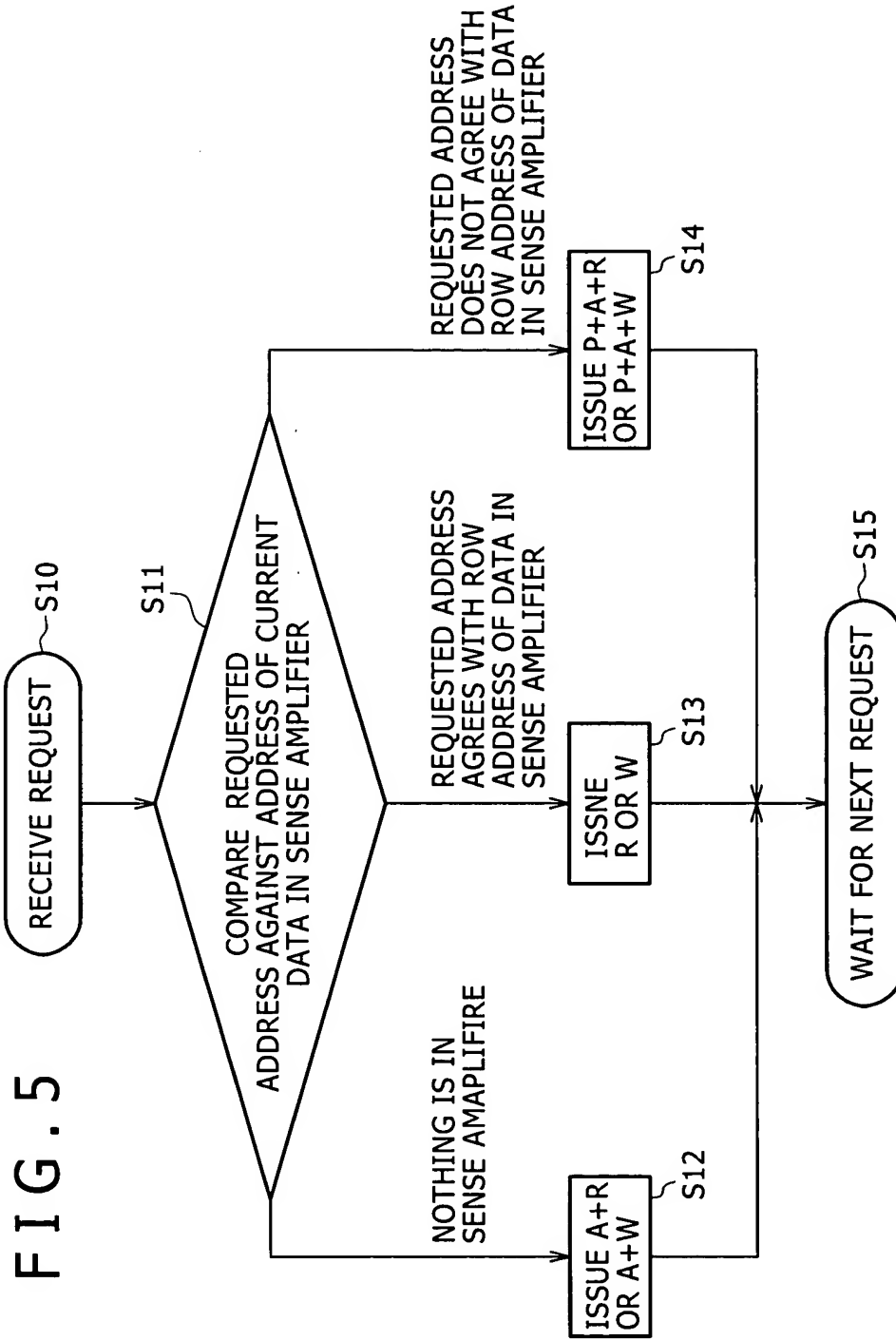
TYPICAL EXTENDED COMPLEX OPERATION PERFORMED BY PRESENT INVENTION



- [A+R+P] ACTIVATE OPERATION+READ OPERATION+PRECHARGE OPERATION
- [P+A+R] PRECHARGE OPERATION+ACTIVATE OPERATION+READ OPERATION
- [R] READ OPERATION
- [A+R] ACTIVATE OPERATION+READ OPERATION

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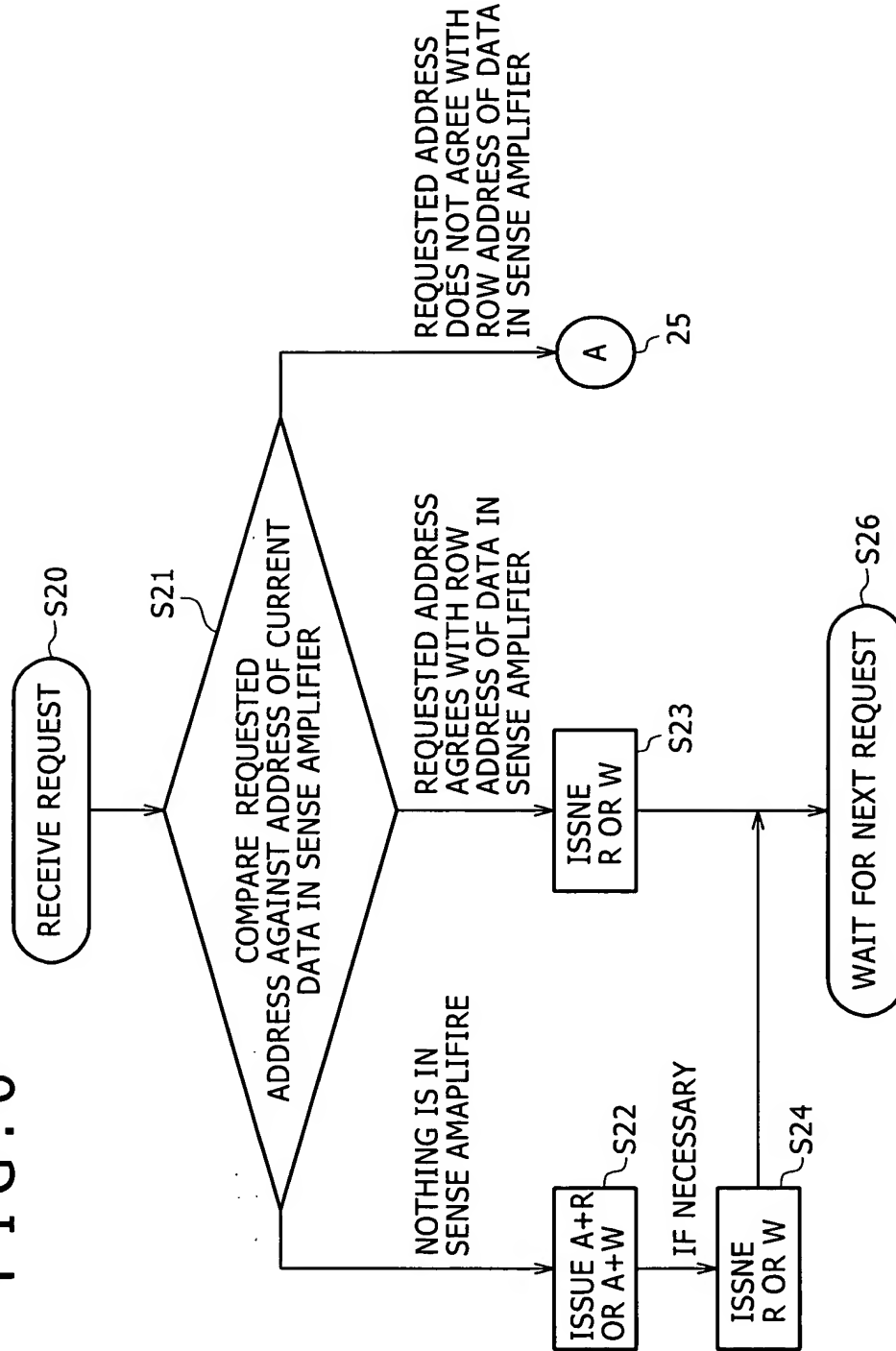
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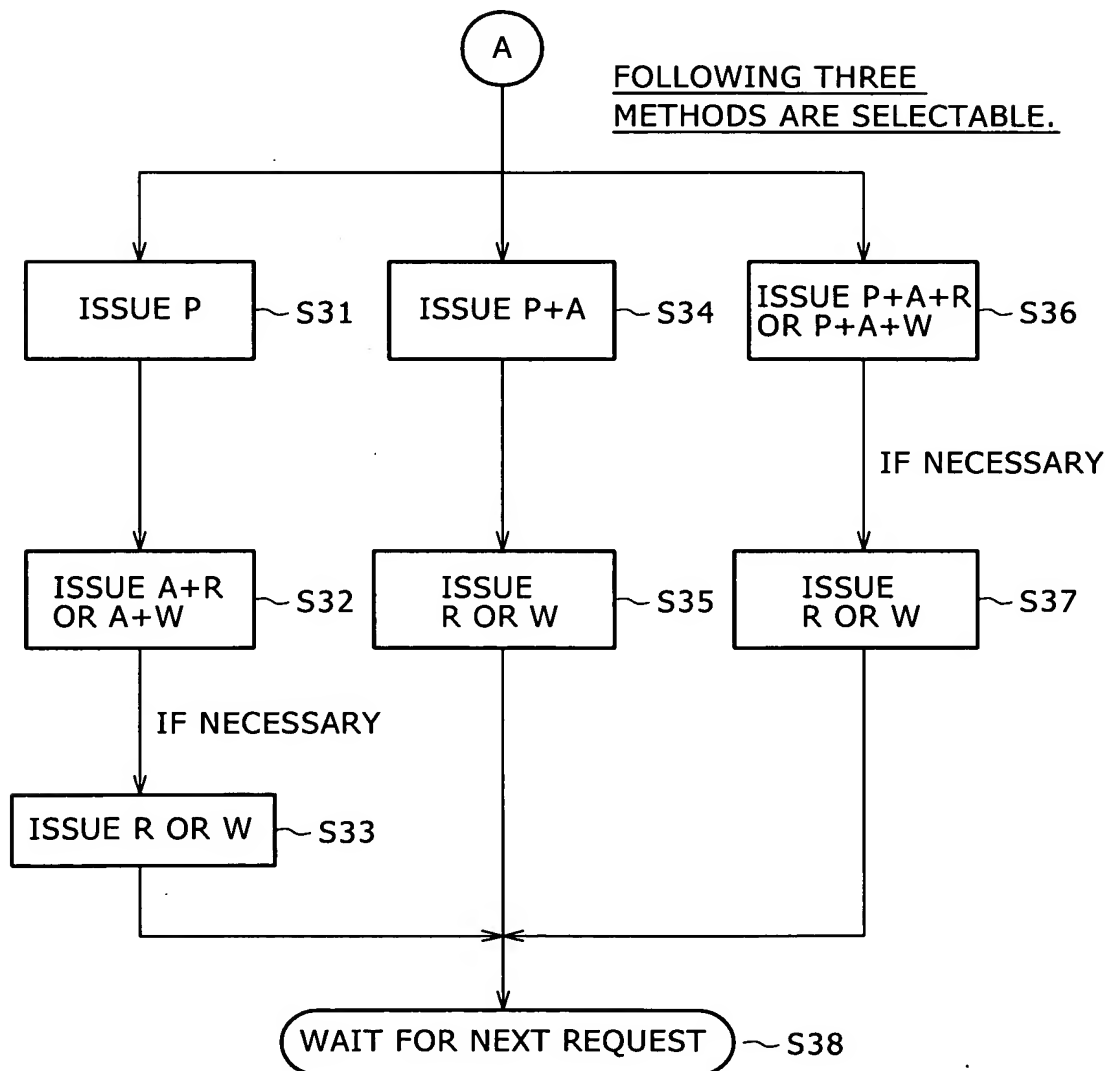
FIG. 6



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FIG. 7



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FIG. 8

